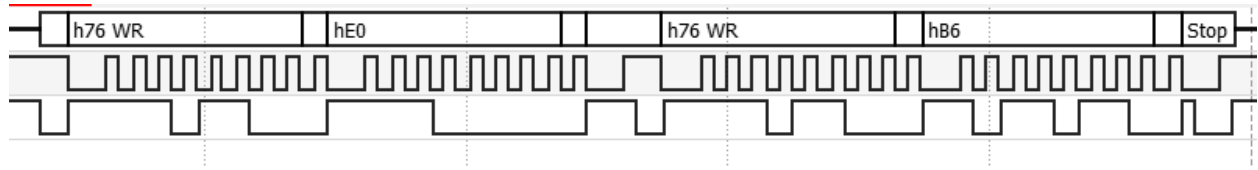


****I2Cwrite****

Length: 1

Interleaved

M: 0 - Reg addr 0xe0 - Data 0xb6



****I2Cwrite****

Length: 9

Interleaved

M: 0 - Reg addr 0x71 - Data 0x80

M: 1 - Reg addr 0x72 - Data 0x00

M: 3 - Reg addr 0x73 - Data 0x00

M: 5 - Reg addr 0x74 - Data 0x20

M: 7 - Reg addr 0x75 - Data 0x00

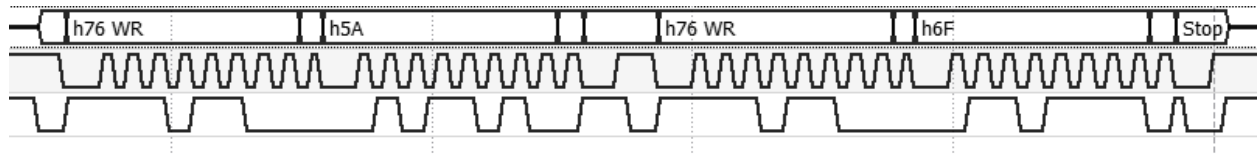


****I2Cwrite****

Length: 1

Interleaved

M: 0 - Reg addr 0x5a - Data 0x6f

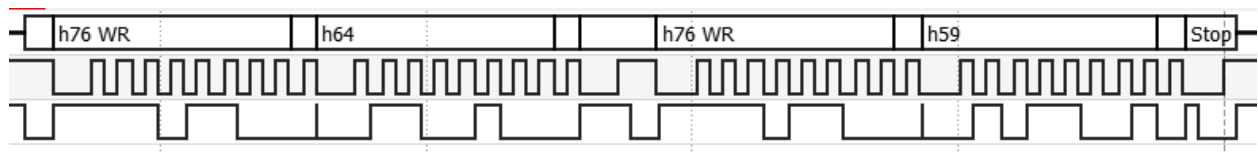


****I2Cwrite****

Length: 1

Interleaved

M: 0 - Reg addr 0x64 - Data 0x59



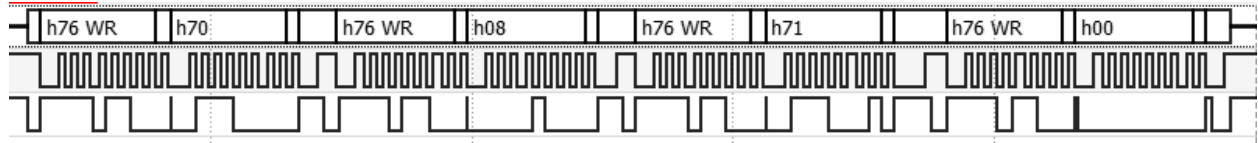
****I2Cwrite****

Length: 3

Interleaved

M: 0 - Reg addr 0x70 - Data 0x08

M: 1 - Reg addr 0x71 - Data 0x00



****I2Cwrite****

Length: 1

Interleaved

M: 0 - Reg addr 0x74 - Data 0x01

