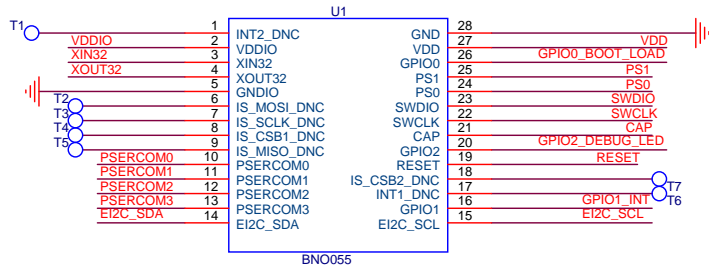
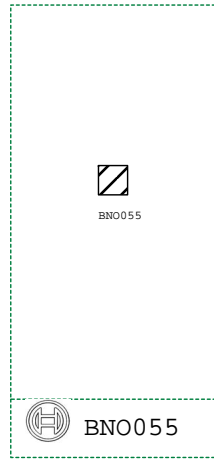


# BNO055 Shuttle

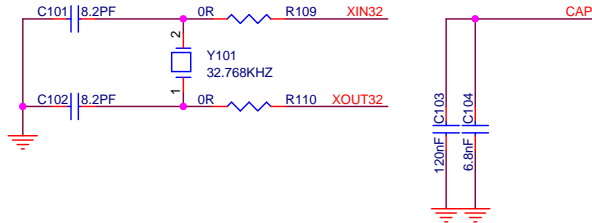


## I2C address Matrix

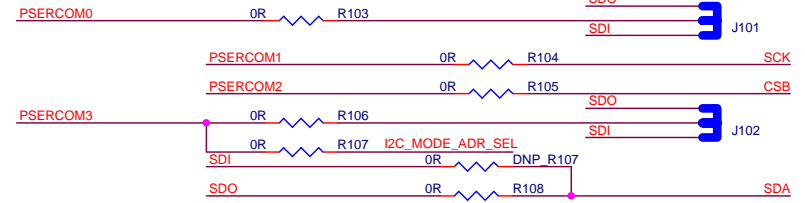
Addr	Part
TBD	BNO055



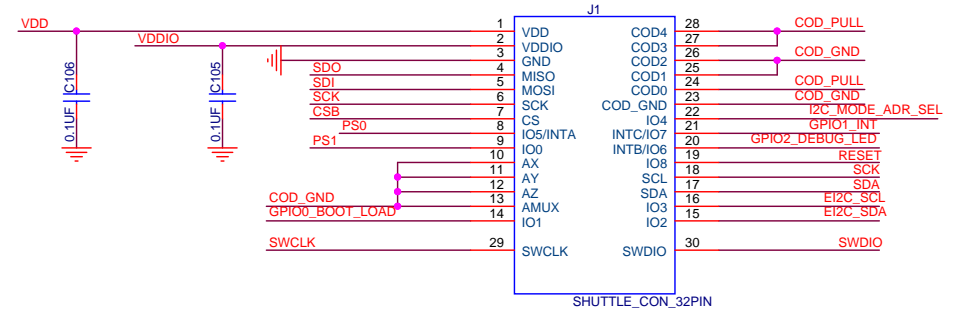
Shuttle placement



SID COD8 -> COD0  
0 0 0 0 1 1 0 0 1



	BNO055 AS MASTER	BNO055 AS SLAVE
PSERCOM IS USED AS SPI BUS	J101 JUMP TO SDI NET	J101 JUMP TO SDO NET
	J102 JUMP TO SDO NET	J102 JUMP TO SDI NET



SPI BUS	PSERCOM0	SDO
	PSERCOM1	SCK
	PSERCOM2	CSB
	PSERCOM3	SDI
SDO(Data Out) and SDI(Data in) for both Master and Slave		

PS1	PS0	FUNCTION	I2C BUS	PSERCOM0	SDA
0	0	I2C INTERFACE		PSERCOM1	SCL
0	1	HID OVER I2C	PSERCOM2	GNDIO	
1	0	UART INTERFACE	PSERCOM3	GNDIO(VDDIO) FOR Primary (alternate) I2C address	
1	1	SPI INTERFACE	SDO(Data Out) and SDI(Data in) for both Master and Slave		

Title <b>BNO055 shuttle</b>			
Size <b>B</b>	Document Number <b>BNO055 SHUTTLE Design: Lu Xinhua Review:Jiao Tongtong Tong Xiaofeng</b>	Rev <b>1.0</b>	
Date: <b>Friday, July 12, 2013</b>	Sheet <b>1</b>	of <b>1</b>	